

Andreas Steininger is associate professor at the Institute of Computer Engineering, specifically in the Embedded Computing Systems group.

In his research he has been involved in many industrial and scientific projects concerned with real-time communication networks, the design of fault-tolerant / radiation-tolerant computer architectures and their evaluation by means of fault-injection, testing (built-in self-test, on-line testing), and asynchronous logic design. His current research focuses on asynchronous ("clockless") logic design, timing-domain interfacing, metastability, and GALS architectures. He has published 150+ papers in journals and at international conferences, and is co-inventor of 10+ patents. Much of his scientific work was done in cooperation with industrial partners like Intel, Bosch, RUAG Space, Thales, Elektrobit, Audi, TTTech, Gleichmann Research, Frequentis, Daimler Chrysler, Elin, Festo etc.

With respect to teaching, Andreas Steininger is contributing to the (Bachelor and Master) curriculum of Computer Engineering at the Faculty of Informatics, mainly with his courses on Digital Design, Advanced Digital Design and HW/SW Codesign. Two of these (co-lectured) courses have been awarded TU Wien's Best Teaching Awards in the categories "Digital Teaching" and "Distance Lecture". Steininger has given invited lectures at international universities and summer schools.

Three of his master students have been winners of the faculty's highly competitive "Distinguished Young Alumnus Award" for the best diploma thesis. On doctoral level, he has supervised 20+ successful PhD theses and serves as the Director for the TU Wien Informatics Doctoral School and as chair of the Doctoral College Resilient Embedded Systems.